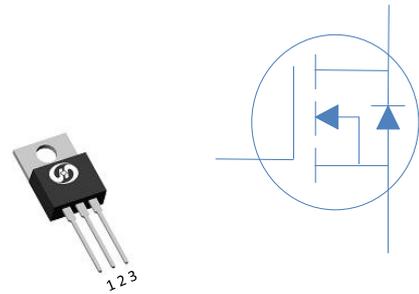


150V N-Ch Power MOSFET

V_{DS}		150	V
$R_{DS(on),typ}$	TO-263	9.4	m
$R_{DS(on),typ}$	TO-220	9.7	m
I_D		91	A



Part Number	Package	Marking
HGB115N15S	TO-263	GB115N15S
HGP115N15S	TO-220	GP115N15S

Absolute Maximum Ratings at $T_J=25^{\circ}\text{C}$ (unless otherwise specified)

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	$T_C=25^{\circ}\text{C}$	91	A
		$T_C=100^{\circ}\text{C}$	64	
Drain to Source Voltage	V_{DS}	-	150	V
Gate to Source Voltage	V_{GS}	-	± 20	V
Pulsed Drain Current	I_{DM}	-	300	A
Avalanche Energy, Single Pulse	E_{AS}	$L=0.4\text{mH}, T_C=25^{\circ}\text{C}$	125	mJ
Power Dissipation	P_D	$T_C=25^{\circ}\text{C}$	214	W
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to 175	$^{\circ}\text{C}$

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Ambient	R_{JA}	60	$^{\circ}\text{C/W}$
Thermal Resistance Junction-Case	R_{JC}	0.7	$^{\circ}\text{C/W}$

				42	-	
				14	-	nC
				7	-	
Turn on Delay Time	$t_{d(on)}$			17	-	
Rise time	t_r	$V_{DD}=75V, I_D=20A, V_{GS}=10V,$		8	-	ns
Turn off Delay Time	$t_{d(off)}$	$R_G=10 \Omega$		26	-	
Fall Time	t_f			10	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_F=20A$		0.9	1.2	V
Reverse Recovery Time	t_{rr}					ns
Reverse Recovery Charge	Q_{rr}	$V_R=75V, I_F=20A, di_F/dt=100A/(\mu s)$				

Fig 1. Typical Output Characteristics



Figure 2. On-Resistance vs. Gate-Source Voltage

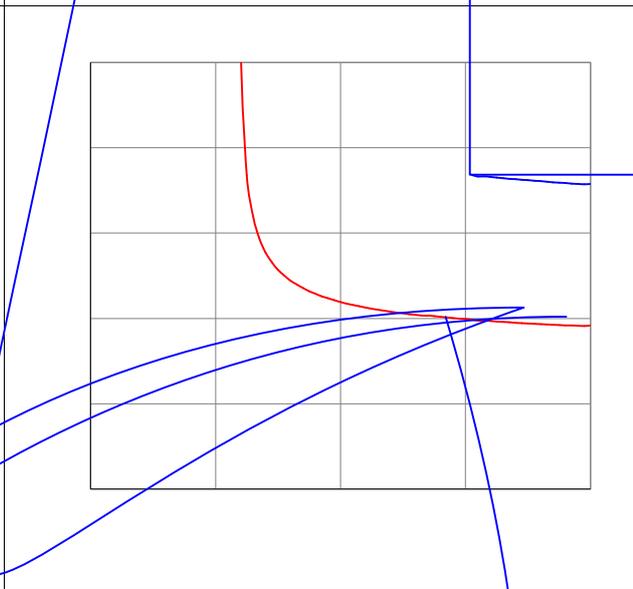


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

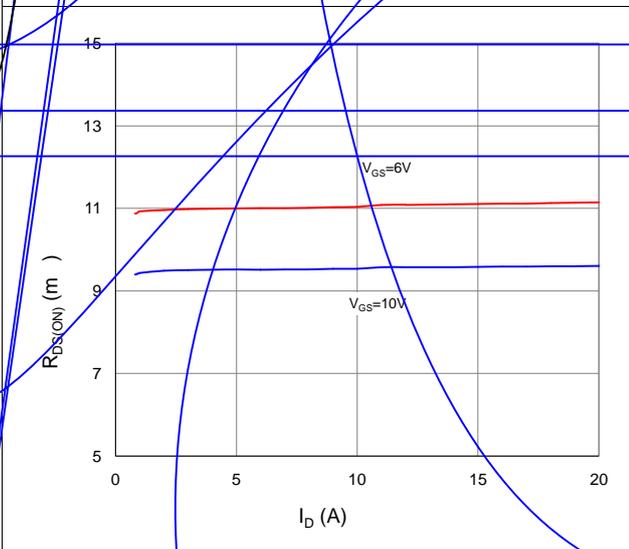


Figure 4. Normalized On-Resistance vs. Junction Temperature

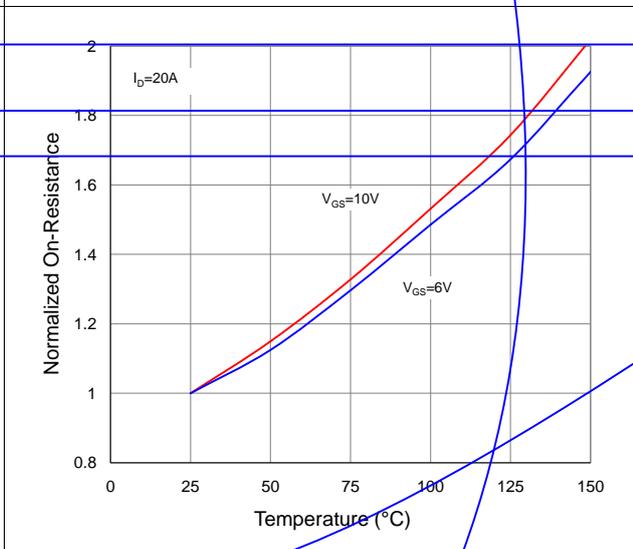


Figure 5. Typical Transfer Characteristics

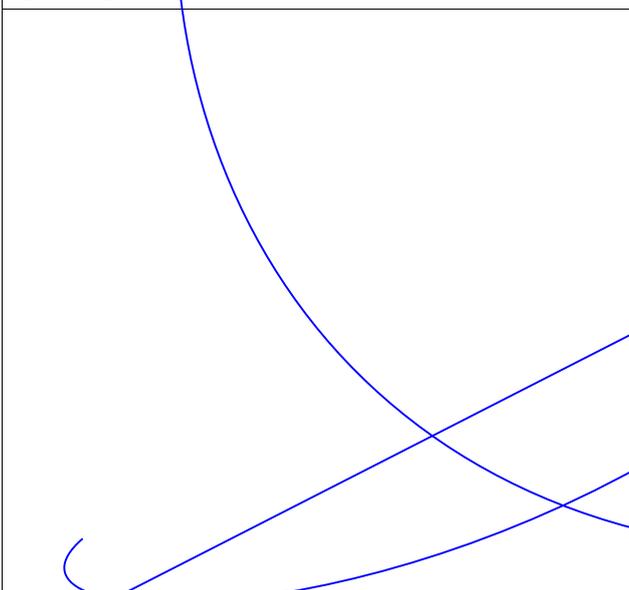


Figure 6. Typical Source-Drain Diode Forward Voltage



Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

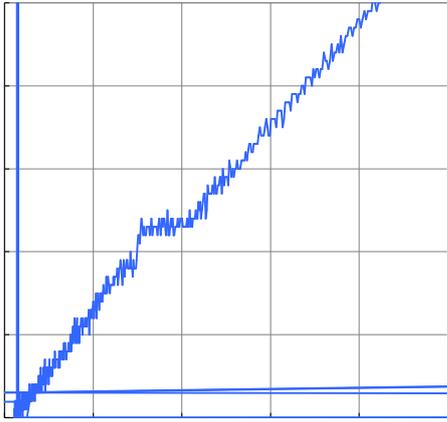


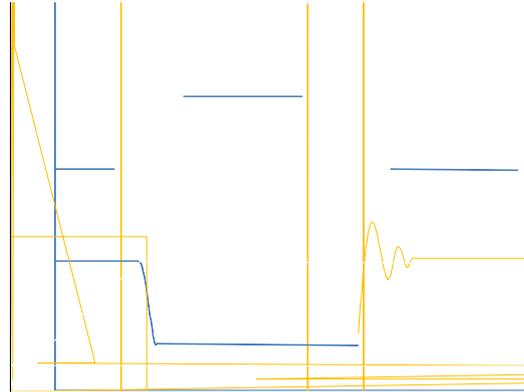
Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

Figure 9. Maximum Safe Operating Area

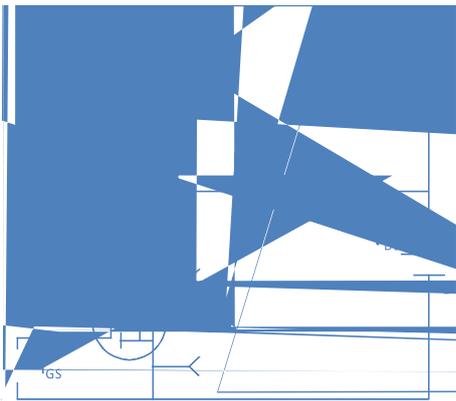
Figure 10. Maximun Drain Current vs. Case Temperature

Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient

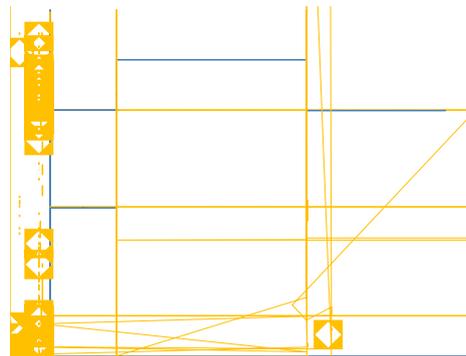
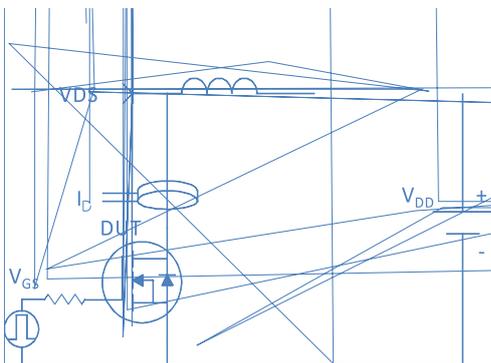
Inductive switching Test



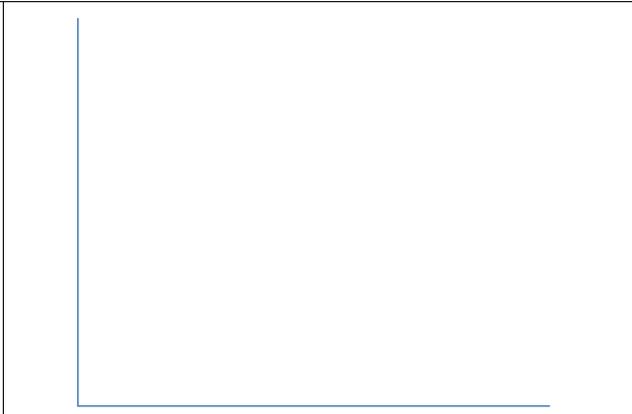
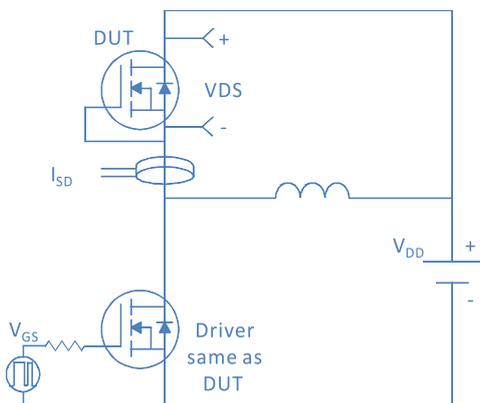
Gate Charge Test



Unclamped Inductive Switching (UIS) Test

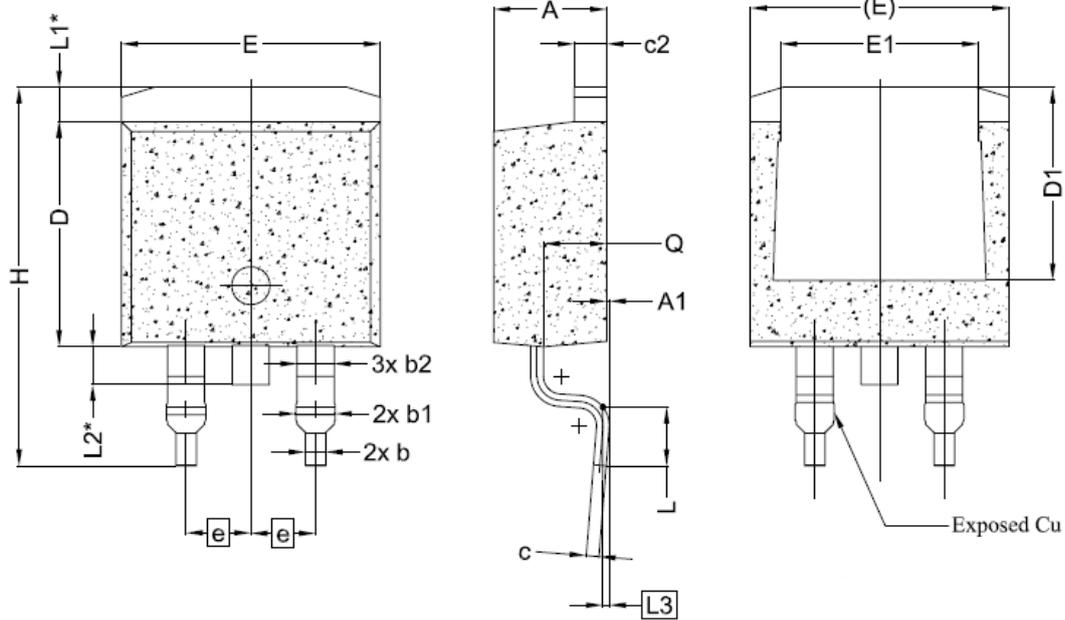


Diode Recovery Test



Package Outline

TO-263, 3 leads



Symbol	Dimension	Value	Symbol	Dimension	Value
A	15.88	15.88	A1	0.85	0.85
A1	0.85	0.85	b1	0.51	0.51
b1	0.51	0.51	b2	0.51	0.51
b2	0.51	0.51	c	0.25	0.25
c	0.25	0.25	c2	0.25	0.25
c2	0.25	0.25	D	1.50	1.50
D	1.50	1.50	D1	0.85	0.85
D1	0.85	0.85	E	2.70	2.70
E	2.70	2.70	E1	2.54	2.54
E1	2.54	2.54	H	4.57	4.57
H	4.57	4.57	L1	1.36	1.36
L1	1.36	1.36	L2	1.50	1.50
L2	1.50	1.50	L3	0.25	0.25
L3	0.25	0.25	Q	0.25	0.25
Q	0.25	0.25			